

**S/N UNKNOWN**

**PATENT**

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6-26-00  
JM

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Brett L. Williams

Examiner: Unknown

Serial No.: Unknown

Group Art Unit: Unknown

Filed: Herewith

Docket: 303.164US3

Title: APPARATUS HAVING MULTIPLE MEMORY MODES

**PRELIMINARY AMENDMENT**

Assistant Commissioner for Patents

Washington, D.C. 20231

Please enter the following amendments and remarks prior to examination of this continuation application:

**IN THE DRAWINGS**

Applicant has attached a red-lined drawing showing a proposed amendment to Figure 9.

Applicant will provide revised formal drawings following the Examiner's approval of the proposed changes.

**IN THE SPECIFICATION**

On page 1, line 7, before "This invention" please insert **A** --This application is a continuation of U.S. Serial No. 08/457,650 filed June 1, 1995, which is a continuation-in-part of U.S. Serial No. 08/386,894 filed February 10, 1995, now U.S. Patent No. 5,610,864, which is a continuation-in-part of U.S. Serial No. 08/370,761 filed December 23, 1994, now U.S. Patent No. 5,526,320.--

At page 28, line 9, before "Memory control circuit 118" please insert **A** --A power up detection circuit 134 may be coupled to microprocessor 112 and to a power supply 136. Power up detection circuit 134 is responsive to a signal from power supply 136 to cause microprocessor 112 to access a nonvolatile memory device to receive information enabling microprocessor 134 to detect the mode of a memory device of memory 124 and to program memory control circuit 118 in accordance with the memory device mode.--